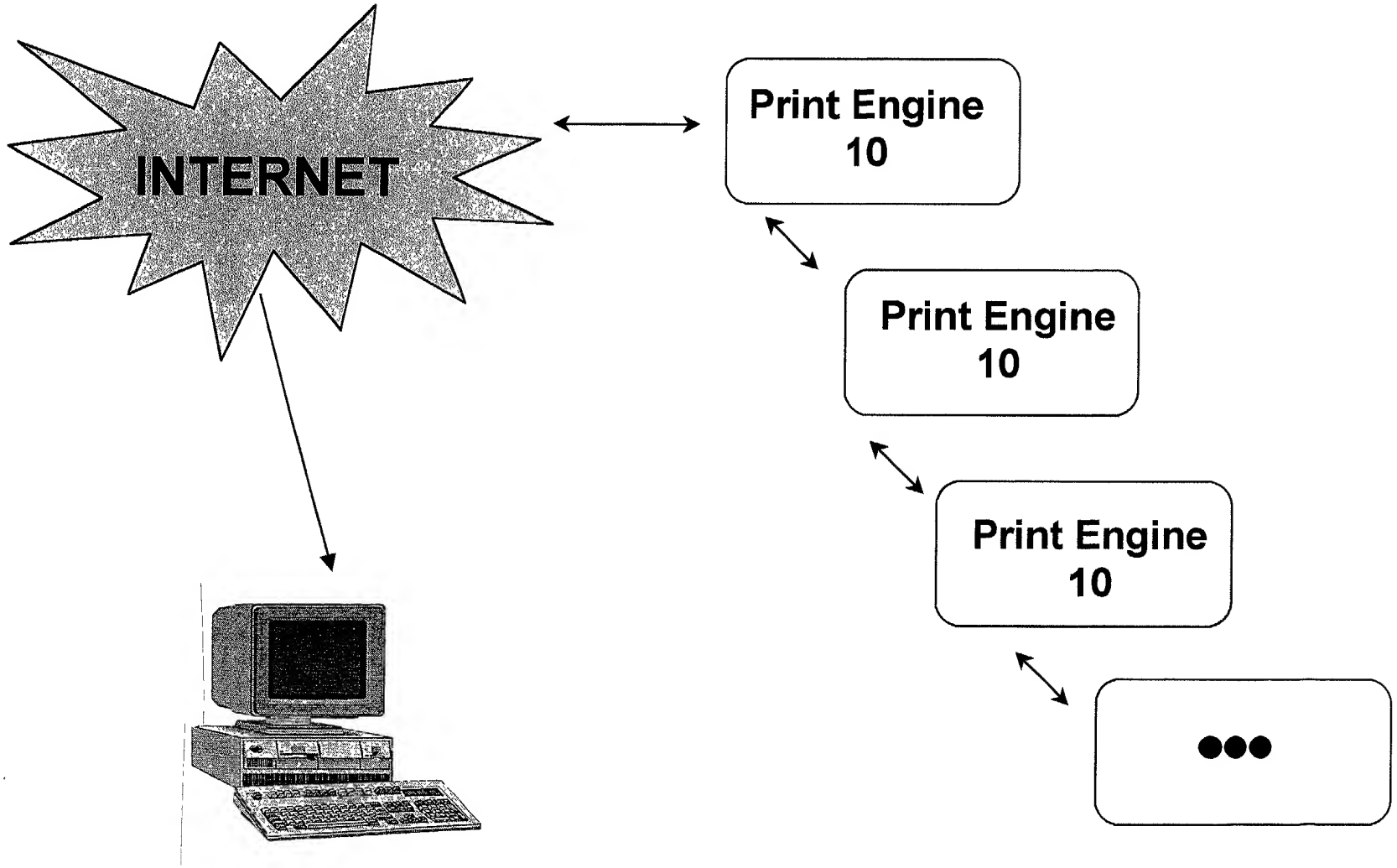
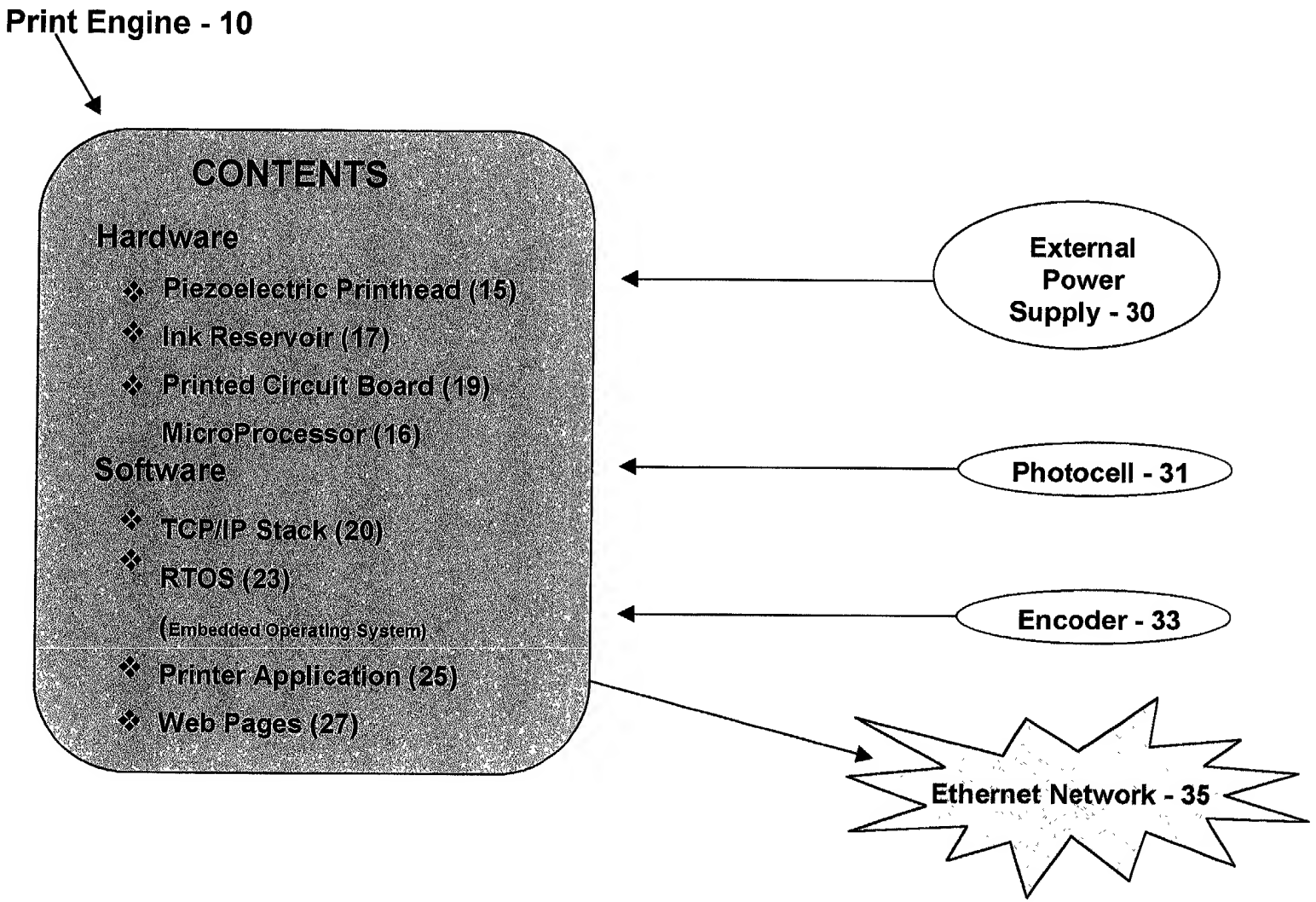
PCT/US2003/033952

FIG. 1



PCT/US2004/035000

FIG. 2



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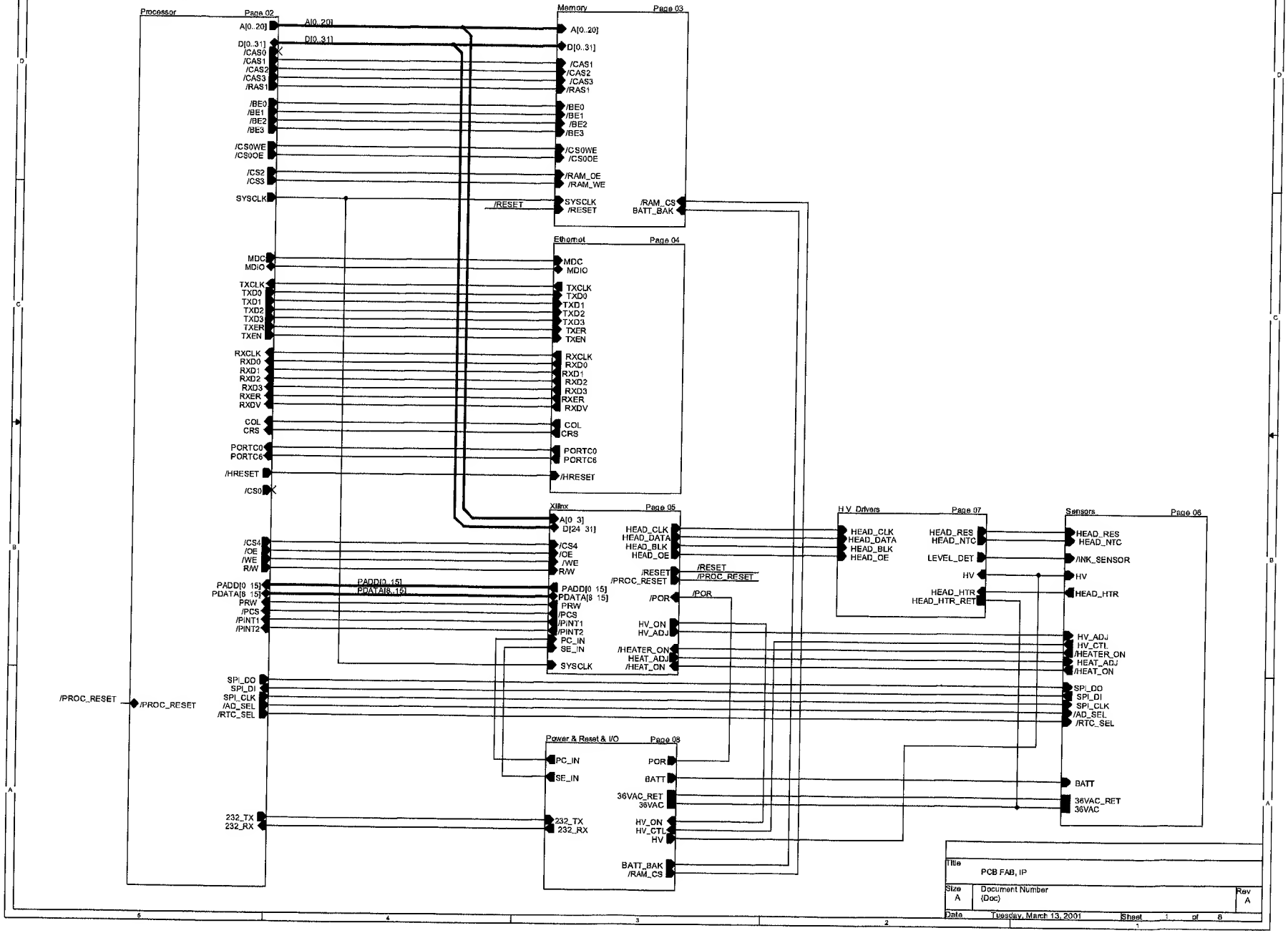

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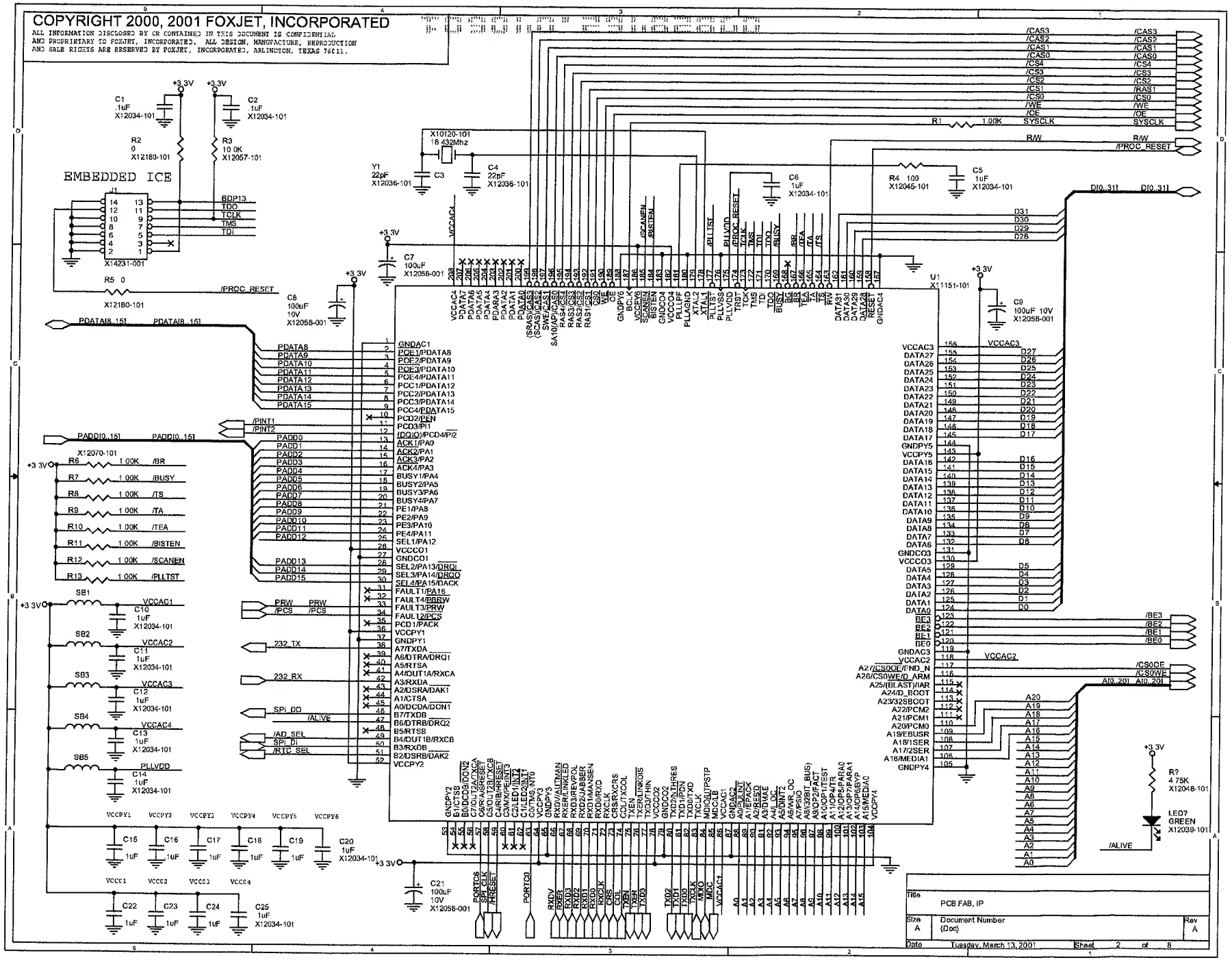

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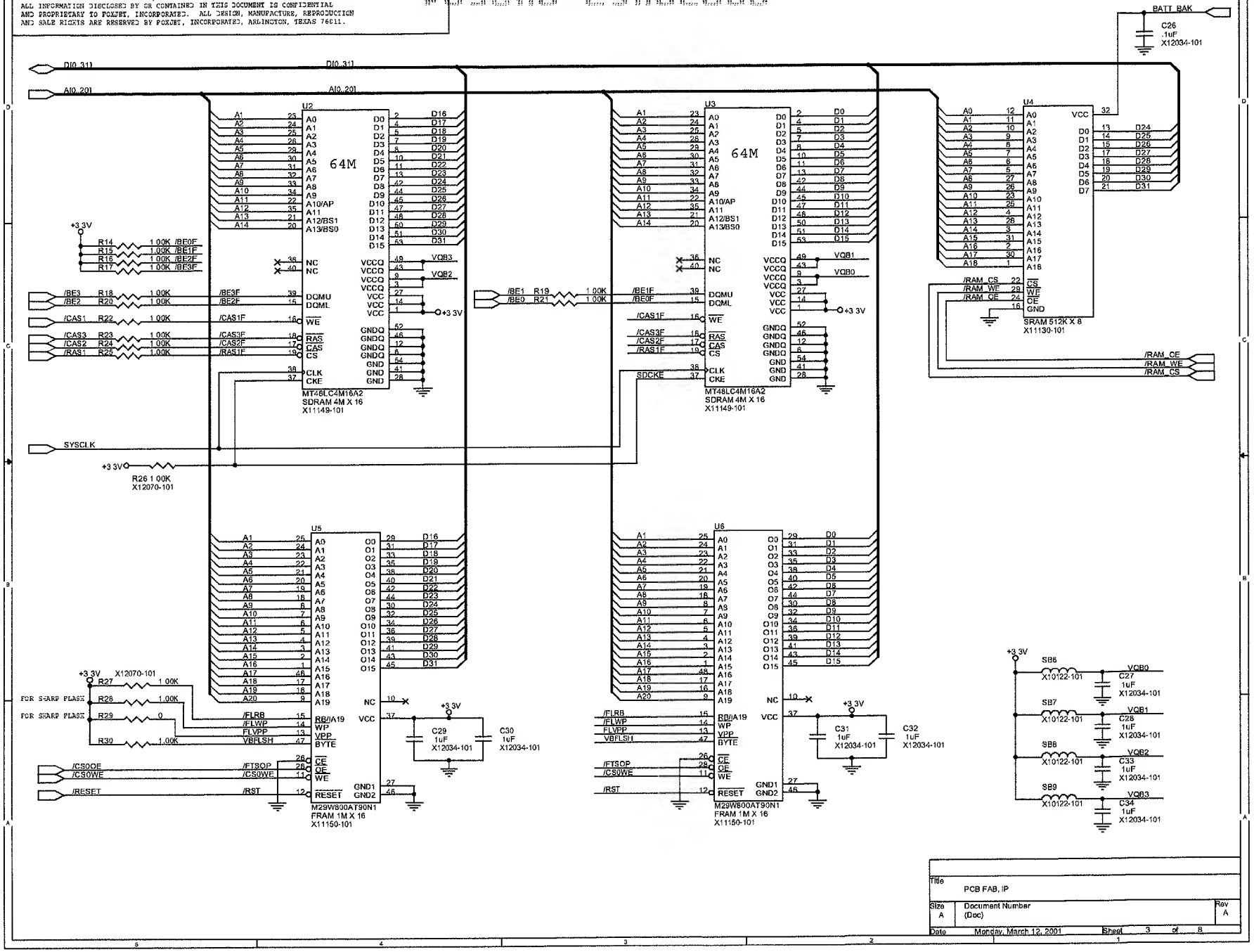

Title	PCB FAB, IP	
Size A	Document Number (Doc)	Rev A
Date	Monday, March 12, 2001	Sheet 3 of 8

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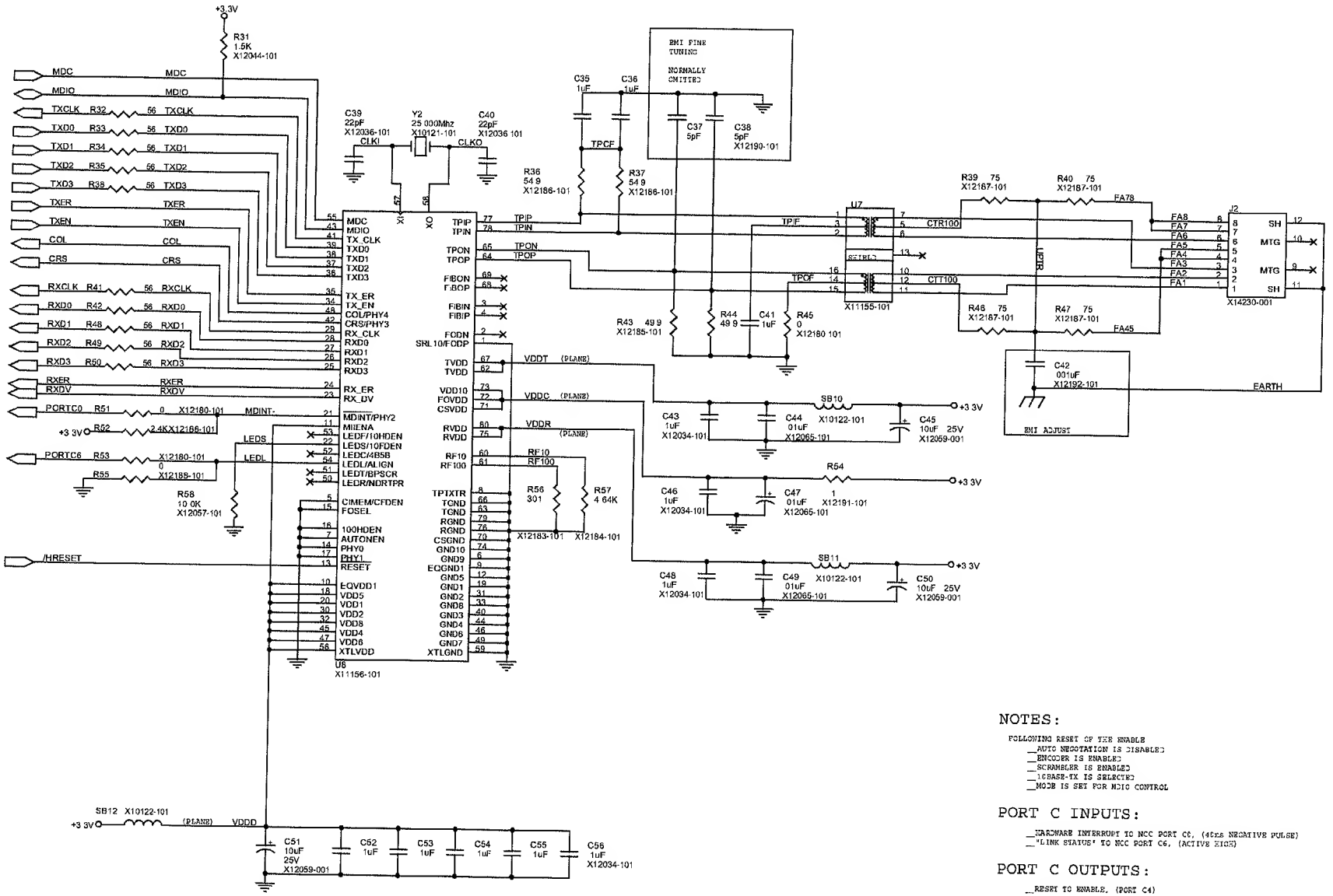

NOTES:

FOLLOWING RESET OF TER ENABLE
__AUTO NEGOTIATION IS DISABLED
__ENCODER IS ENABLED
__SCRAMBLER IS ENABLED
__10BASE-TX IS SELECTED
__MODE IS SET FOR MDIO CONTROL

PORT C INPUTS:

__HARDWARE INTERRUPT TO NCC PORT C0, (40ns NEGATIVE PULSE)
__"LINK STATUS" TO NCC PORT C6, (ACTIVE HIGH)

PORT C OUTPUTS:

__RESET TO ENABLE. (PORT C4)

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FIG. 4

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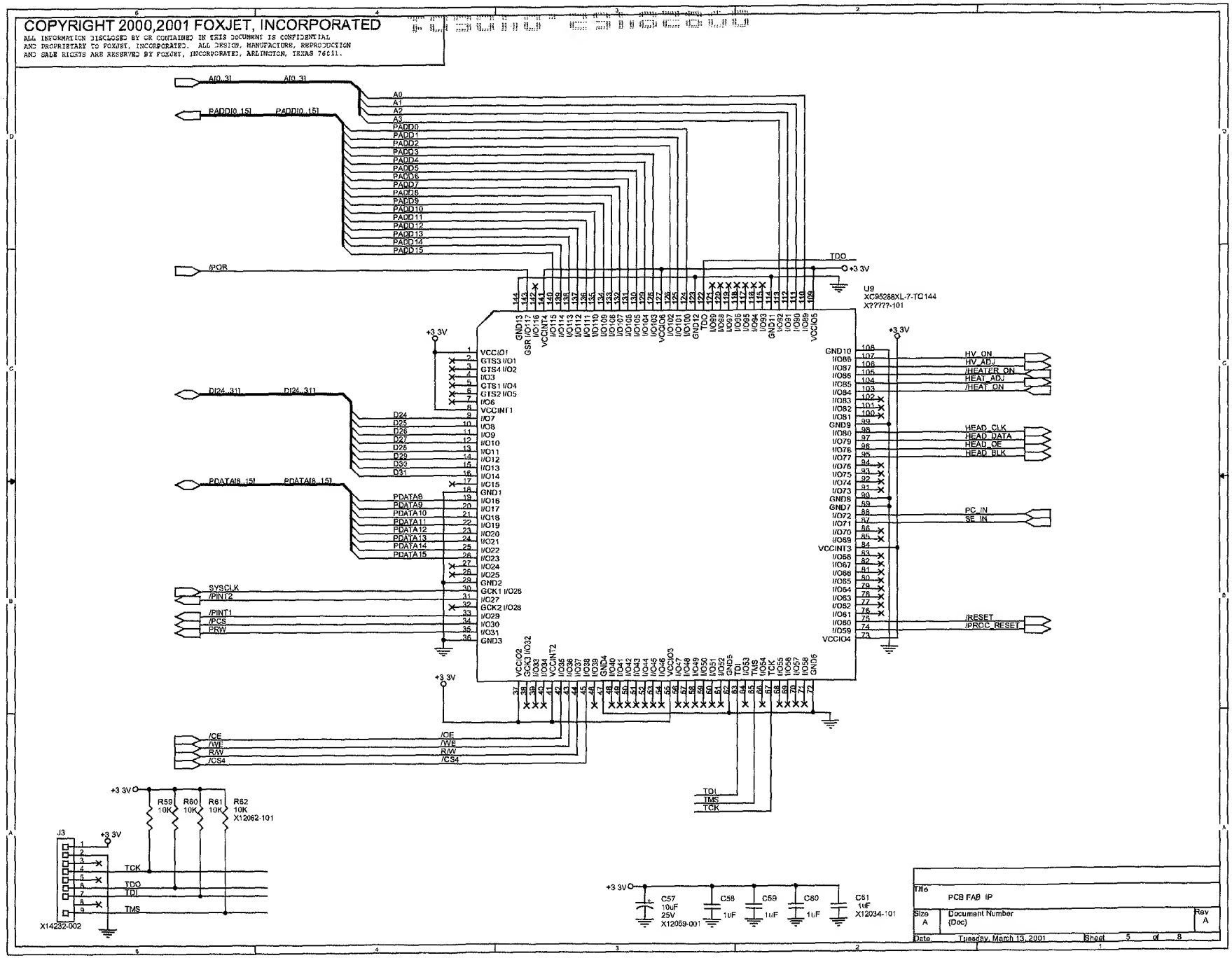

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FIG. 8

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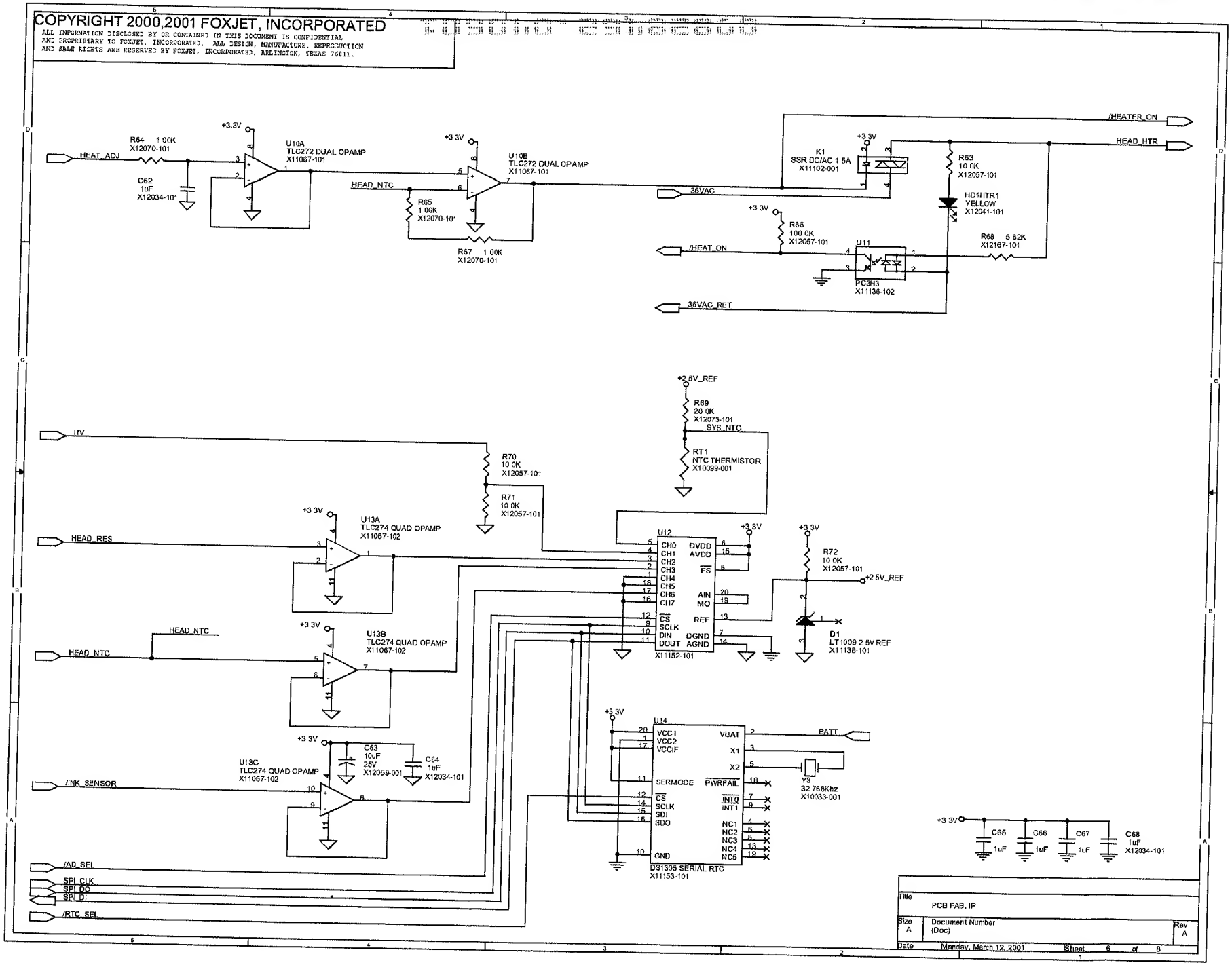

FIG. 7

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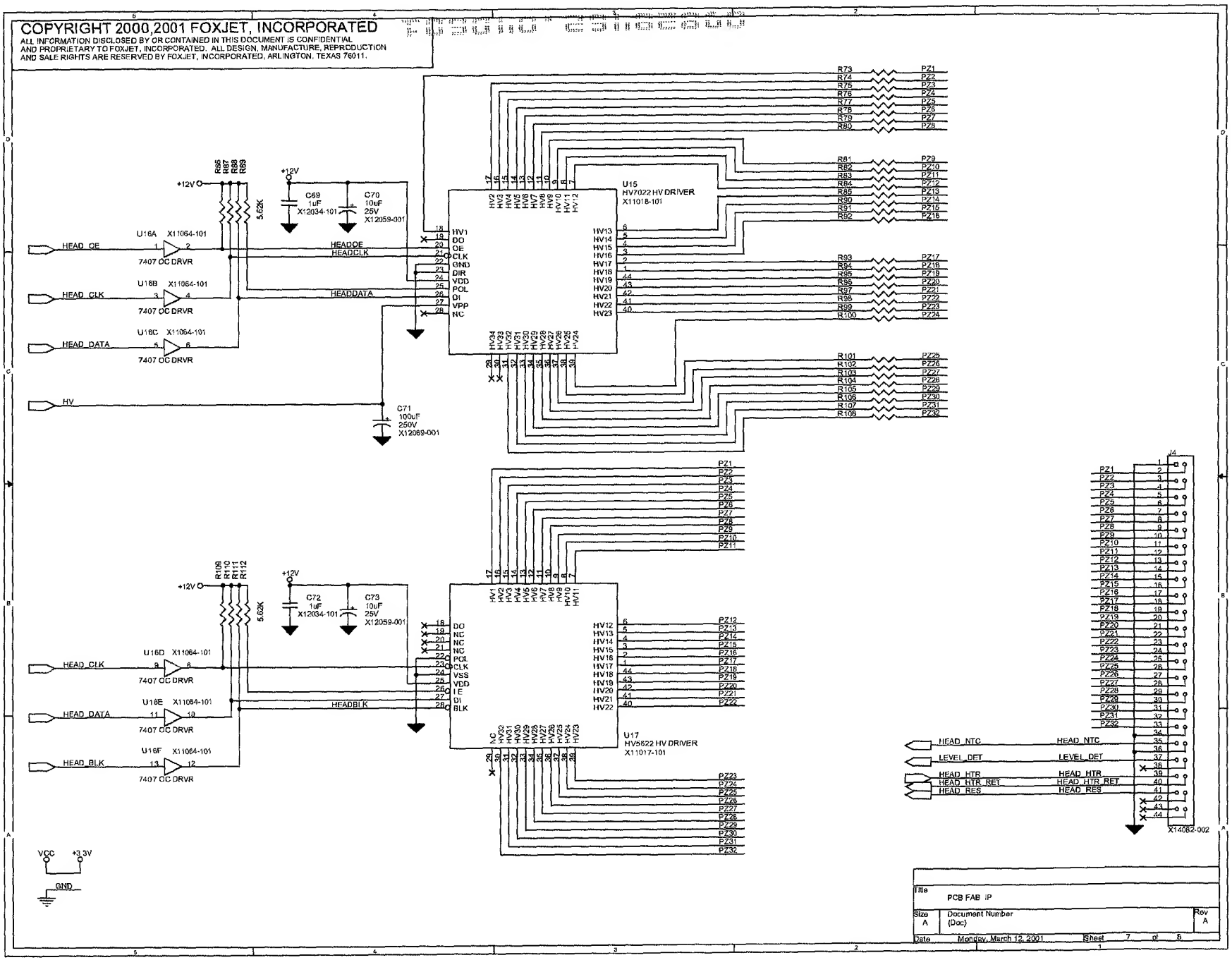
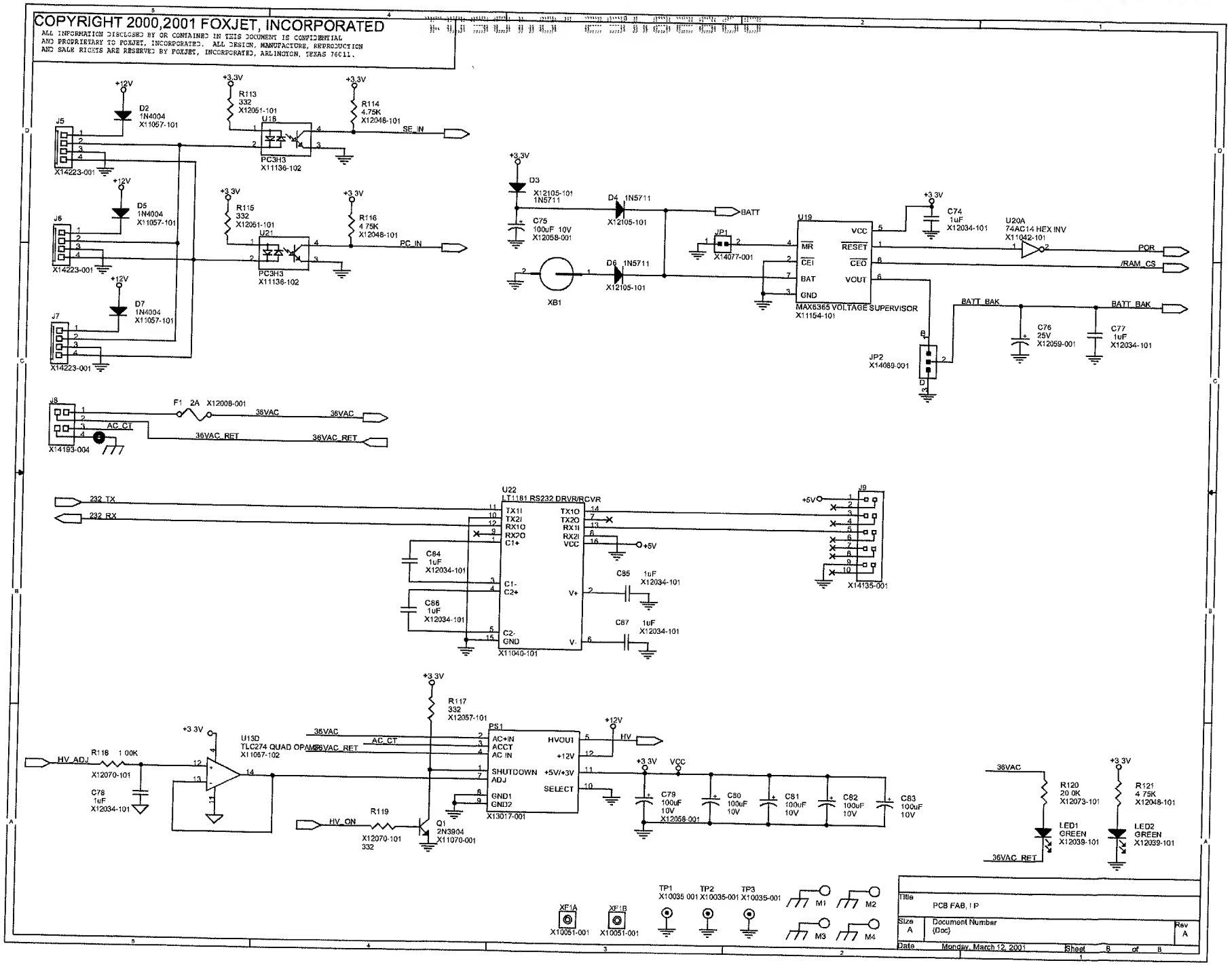


Title	PCB FAB JP	
Size A	Document Number (Doc)	Rev A
Date	Monday, March 12, 2001	Sheet 7 of 8

FIG. 10

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